

Docket No. 250327US-2S

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

INVENTOR(S) Shinichi YOSHIOKA

SERIAL NO: 10/798,403

ART UNIT: 2611

FILING DATE: March 12, 2004

EXAMINER: FLORES, LEON

FOR: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND METHOD OF TESTING THE SAME

**FEE TRANSMITTAL**

- ☒ No additional fee is required  
☐ Small entity status of this application under 37 C.F.R. §1.9 and §1.27 is claimed.

The Fee has been calculated as shown below:

FOR	NUMBER FILED	NUMBER EXTRA	RATE	CALCULATIONS
TOTAL CLAIMS	19 - 20 =	0	x \$52 =	\$0.00
INDEPENDENT CLAIMS	3 - 3 =	0	x \$220 =	\$0.00
<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS (If applicable)			+ \$390 =	\$0.00
<input type="checkbox"/> LATE FILING OF DECLARATION			+ \$130 =	\$0.00
BASIC FEES				\$0.00
TOTAL OF ABOVE CALCULATIONS				\$0.00
<input type="checkbox"/> REDUCTION BY 50% FOR FILING BY SMALL ENTITY				\$0.00
<input type="checkbox"/> FILING IN NON-ENGLISH LANGUAGE			+ \$130 =	\$0.00
TOTAL				\$0.00

- ☐ Please charge Deposit Account No. **15-0030** in the amount of **\$0.00**  
☐ Credit card payment is being made online (if electronically filed), or is attached hereto (if paper filed), in the amount of **\$0.00**.  
☒ The Director is hereby authorized to charge any additional fees which may be required for the papers being filed herewith and for which no payment is enclosed herewith, or credit any overpayment to Deposit Account No. **15-0030**.  
☒ If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time may be charged to Deposit Account No. 15-0030.

Submitted by: \_\_\_\_\_

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